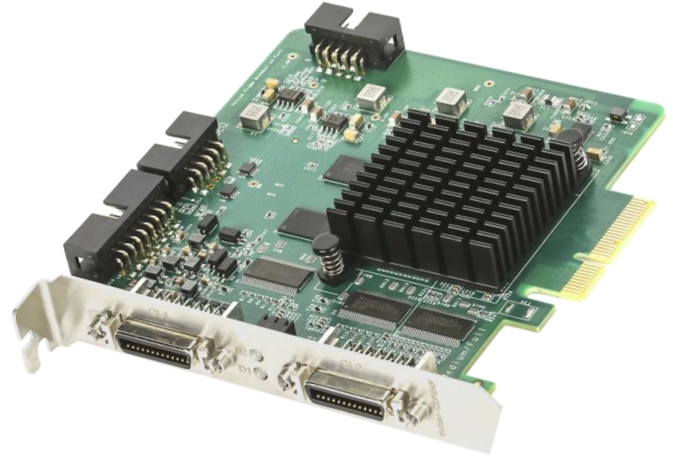




# Frame grabber Vulcan-CL PE4 Full

## Features

- ◎ Half-length substrate
- ◎ Full/Full+/Base/Medium
- ◎ PCIe Gen 2.0 and Camera Link 2.0  
(backward compatible)
- ◎ 20-85MHz pixel clock
- ◎ Support Mono/Bayer/Color mode
- ◎ Support 8/10/12/14/16 bit multiple different pixel depths
- ◎ Frame grabber power < 6W
- ◎ Maximum horizontal resolution 64k pixels
- ◎ Support external trigger signal input, encoder signal input and flash signal output
- ◎ Support multi-card synchronization, support firmware online upgrade
- ◎ Support Windows & Linux system
- ◎ Rich secondary development library (C/C++/C#)





## Specifications

| Model                       | Vulcan-CL PE4 Full                                                                                                                                                                                                                                                                                                              |
|-----------------------------|---------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------|
| Camera Link mode            | Full/Full+/Base/Medium                                                                                                                                                                                                                                                                                                          |
| Interface Standard          | Camera link 2.0                                                                                                                                                                                                                                                                                                                 |
| Pixel Septh                 | 8/10/12/14/16 bit                                                                                                                                                                                                                                                                                                               |
| Tap Arrangement             | 1 Tap-8/10/12/14/16-bit Mono/Bayer<br>1 Tap-8/10/12-bit RGB/BGR/RGBC/BGRC<br>2 Taps-8/10/12/14/16-bit Mono/Bayer<br>2 Taps-8/10/12-bit RGB/BGR/RGBC/BGRC<br>3 Taps-8/10/12/14/16-bit Mono/Bayer<br>3 Taps-8/10/12-bit RGB/BGR<br>4 Taps--8/10/12/14/16-bit Mono/Bayer<br>8 Taps-8/10-bit Mono/Bayer<br>10 Taps-8-bit Mono/Bayer |
| Scan Type                   | Area scan / Line scan                                                                                                                                                                                                                                                                                                           |
| Resolution Support          | Level(min/max): 128 byte / 64k bytes;<br>Vertical(min/max): 1 line / 256M lines                                                                                                                                                                                                                                                 |
| Pixel Clock                 | 20MHz - 85MHz                                                                                                                                                                                                                                                                                                                   |
| Onboard Memory              | 512 MB                                                                                                                                                                                                                                                                                                                          |
| Output Data Rate            | 850 MB/s(max)                                                                                                                                                                                                                                                                                                                   |
| Communication Method        | Support computer serial communication 4800-921600bps                                                                                                                                                                                                                                                                            |
| Trigger Input               | Support 2 signals inputs(photoelectric isolation);<br>Maximum frequency 68kHz;<br>Level standard supports RS422;<br>TTL and differential input (3.3V-24V)                                                                                                                                                                       |
| Encoder Input               | Support 1 orthogonal AB phase input (photoelectric isolation);<br>Maximum frequency 1MHz;<br>Level standard supports RS422;<br>TTL and differential input (3.3V-24V)                                                                                                                                                            |
| Inter-board Synchronization | Support multi-card synchronization (up to 4 capture cards);<br>The synchronization delay is not more than 25ns                                                                                                                                                                                                                  |
| Flash Control               | Support 1 signal output (photoelectric isolation);<br>Maximum frequency 80kHz                                                                                                                                                                                                                                                   |
| Secondary Development       | Complete SDK packages; Supporting C/C++/C# language                                                                                                                                                                                                                                                                             |
| System Requirement          | Operating System: Windows 7/10 32/64bit;<br>Hardware: PCIe2.0/1.1 ×1 Slot                                                                                                                                                                                                                                                       |
| Connector                   | MDR                                                                                                                                                                                                                                                                                                                             |