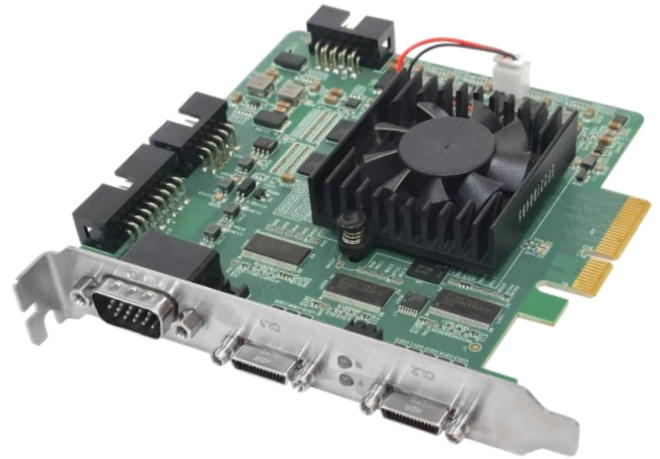




Frame grabber Vulcan-sCL PE4 DBase

Features

- ◎ Half-length substrate
- ◎ Base/Dual-Base
- ◎ PCIe Gen 2.0 and Camera Link 2.0
(backward compatible)
- ◎ 20-85MHz pixel clock
- ◎ Support Mono/Bayer/Color mode
- ◎ Support 8/10/12/14/16 bit multiple different pixel depths
- ◎ Frame grabber power < 6W
- ◎ Maximum horizontal resolution 64k pixels
- ◎ Support external trigger signal input, encoder signal input and flash signal output
- ◎ Support multi-card synchronization, support firmware online upgrade
- ◎ Support Windows & Linux system
- ◎ Rich secondary development library (C/C++/C#)





Specifications

Model	Vulcan-sCL PE4 DBase
Camera Link mode	Base/Dual-Base
Interface Standard	Camera link 2.0
Pixel Septh	8/10/12/14/16 bit
Tap Arrangement	1 Tap-8/10/12/14/16-bit Mono/Bayer 2 Taps-8/10/12/14/16-bit Mono/Bayer 1 Tap-8-bit RGB/BGR
Scan Type	Area scan / Line scan
Resolution Support	Level (min/max): 128 byte / 64k bytes; Vertical(min/max): 1 line /(Area array 256M/horizontal resolution), (Line scan unlimited)
Pixel Clock	20MHz - 85MHz
Onboard Memory	512 MB
Output Data Rate	850 MB/s(max)
Communication Method	Support computer serial communication; 1200-921600bps
Trigger Input	Support 2 signals inputs(photoelectric isolation); Maximum frequency 68kHz; Level standard supports RS422; TTL and differential input (3.3V-24V)
Encoder Input	Support 1 orthogonal AB phase input (photoelectric isolation); Maximum frequency 1MHz; Level standard supports RS422; TTL and differential input (3.3V-24V)
Inter-board Synchronization	Support multi-card synchronization (up to 4 capture cards); The synchronization delay is not more than 25ns
Flash Control	Support 1 signal output (photoelectric isolation); Maximum frequency 80kHz
Secondary Development	Complete SDK packages;Supporting C/C++/C# language
System Requirement	Operating System: Windows 7/10 32/64 bit;Linux; Hardware: PCIe 2.0 × 4 Slot
Connector	SDR